

## 30mA, 600V N-CHANNEL DEPLETION-MODE FIELD-EFFECT TRANSISTOR

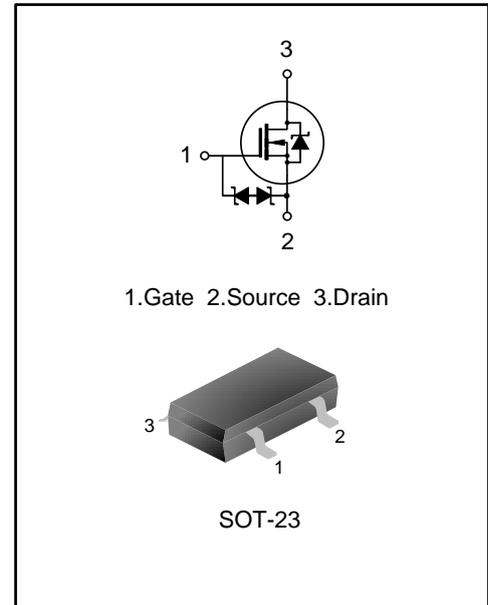
### GENERAL DESCRIPTION

SVD501DEAG is an N-channel depletion-mode power MOS field effect transistor which is produced using Silan VDMOS technology. The improved process and cell structure have been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

It's widely used in electronic ballasts and low power SMPS.

### FEATURES

- ◆ 30mA, 600V
- ◆ Depletion-mode ( Normally-on)
- ◆ Improved ESD ability
- ◆ Fast switching
- ◆ Improved dv/dt capability



### ORDERING INFORMATION

| Part No.     | Package | Marking | Hazardous substance control | Packing Type |
|--------------|---------|---------|-----------------------------|--------------|
| SVD501DEAGTR | SOT-23  | 501DE   | Halogen free                | Tape & Reel  |

**ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C UNLESS OTHERWISE NOTED)**

| Characteristics  | Symbol           | Rating               | Unit  |
|--|------------------|----------------------|-------|
| Drain-Source Voltage   | V <sub>DS</sub>  | 600                  | V     |
| Gate-Source Voltage  | V <sub>GS</sub>  | ±20                  | V     |
| Drain Current  | I <sub>D</sub>   | T <sub>C</sub> =25°C | 0.03  |
|  |                  | T <sub>C</sub> =70°C | 0.024 |
| Drain Current Pulsed   | I <sub>DM</sub>  | 0.12                 | A     |
| Power Dissipation(T <sub>C</sub> =25°C)<br>- Derate above 25°C | P <sub>D</sub>   | 0.8                  | W     |
|  |                  | 0.007                | W/°C  |
| Operation Junction Temperature Range                           | T <sub>J</sub>   | -55~+150             | °C    |
| Storage Temperature Range                                      | T <sub>stg</sub> | -55~+150             | °C    |

**THERMAL CHARACTERISTICS**

| Characteristics                         | Symbol           | Rating | Unit |
|---|------------------|--------|------|
| Thermal Resistance, Junction-to-Case    | R <sub>θJC</sub> | 150    | °C/W |
| Thermal Resistance, Junction-to-Ambient | R <sub>θJA</sub> | 250    | °C/W |

**ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C UNLESS OTHERWISE NOTED)**

| Characteristics                          | Symbol              | Test conditions                                      | Min. | Typ. | Max. | Unit |
|--|---------------------|--|------|------|------|------|
| Drain -Source Breakdown Voltage          | BV <sub>DSS</sub>   | V <sub>GS</sub> =-5V, I <sub>D</sub> =250μA          | 600  | --   | --   | V    |
| Drain-Source Leakage Current             | I <sub>D(off)</sub> | V <sub>DS</sub> =600V, V <sub>GS</sub> =-5V          | --   | --   | 0.1  | μA   |
| Gate-Source Leakage Current              | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V           | --   | --   | ±10  | μA   |
| Gate Threshold Voltage                   | V <sub>GS(th)</sub> | V <sub>DS</sub> =3V, I <sub>D</sub> =8μA             | -2.7 | --   | -1.0 | V    |
| On-state drain current                   | I <sub>DSS</sub>    | V <sub>GS</sub> =0V, V <sub>DS</sub> =25V            | 12   | --   | --   | mA   |
| Static Drain- Source On State Resistance | R <sub>DS(on)</sub> | V <sub>GS</sub> =0V, I <sub>D</sub> =3mA             | --   | 310  | 500  | Ω    |
|  |                     | V <sub>GS</sub> =10V, I <sub>D</sub> =16mA           | --   | 330  | 500  |      |
| Input Capacitance                        | C <sub>ISS</sub>    | V <sub>DS</sub> =25V, V <sub>GS</sub> =-5V, f=1.0MHz | --   | 10   | --   | pF   |
| Output Capacitance                       | C <sub>OSS</sub>    |  | --   | 2.9  | --   |      |
| Reverse Transfer Capacitance             | C <sub>RSS</sub>    |  | --   | 0.12 | --   |      |
| Turn-on Delay Time                       | t <sub>d(on)</sub>  | V <sub>DD</sub> =300V, I <sub>D</sub> =0.01A         | --   | 12   | --   | ns   |
| Turn-on Rise Time                        | t <sub>r</sub>      | V <sub>GS</sub> =-5~7V                               | --   | 60   | --   |      |
| Turn-off Delay Time                      | t <sub>d(off)</sub> | R <sub>G</sub> =6Ω                                   | --   | 25   | --   |      |
| Turn-off Fall Time                       | t <sub>f</sub>      | (Note 1,2)   | --   | 100  | --   |      |
| Total Gate Charge                        | Q <sub>g</sub>      | V <sub>DS</sub> =400V, I <sub>D</sub> =0.01A         | --   | 2.8  | --   | nC   |
| Gate-Source Charge                       | Q <sub>gs</sub>     | V <sub>GS</sub> =-5V~7V                              | --   | 0.55 | --   |      |
| Gate-Drain Charge                        | Q <sub>gd</sub>     | (Note 1,2)   | --   | 1.6  | --   |      |

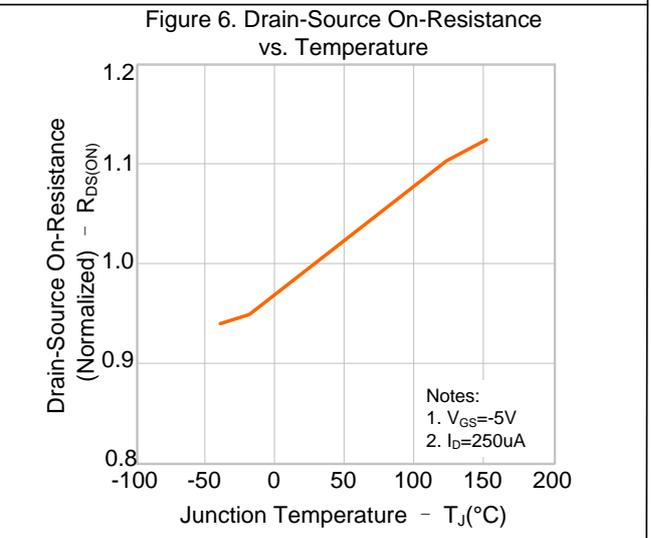
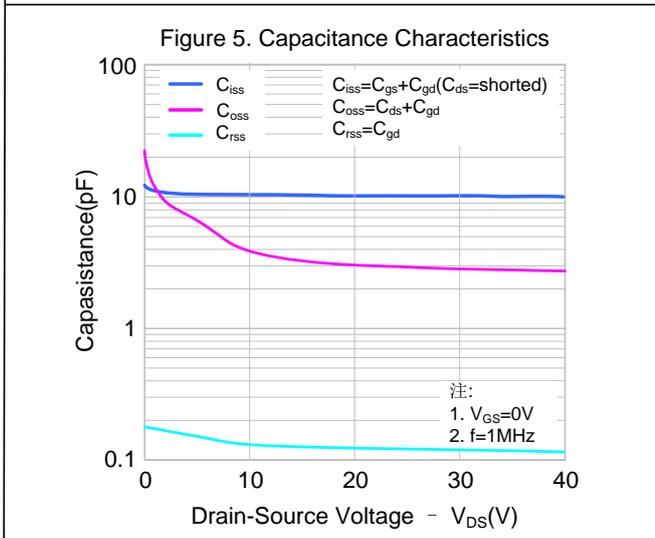
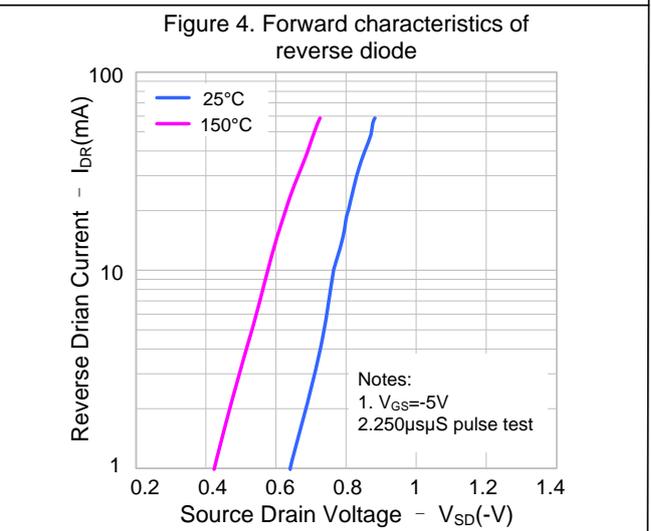
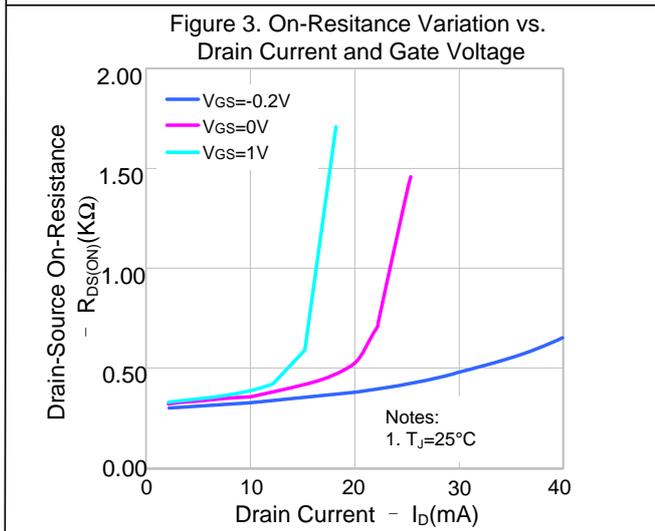
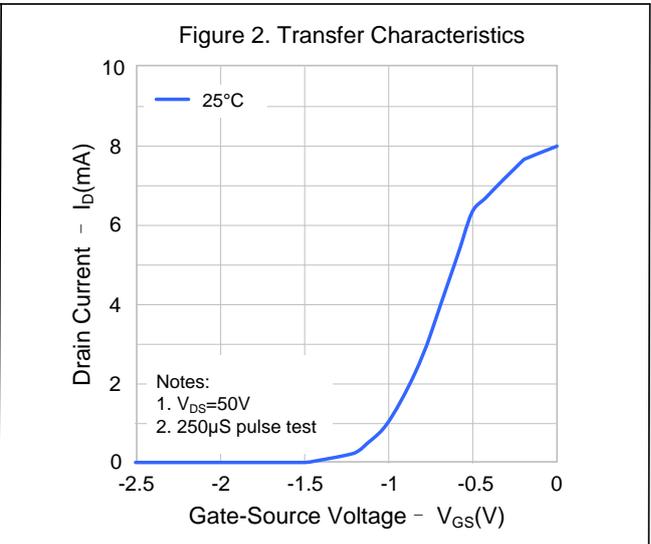
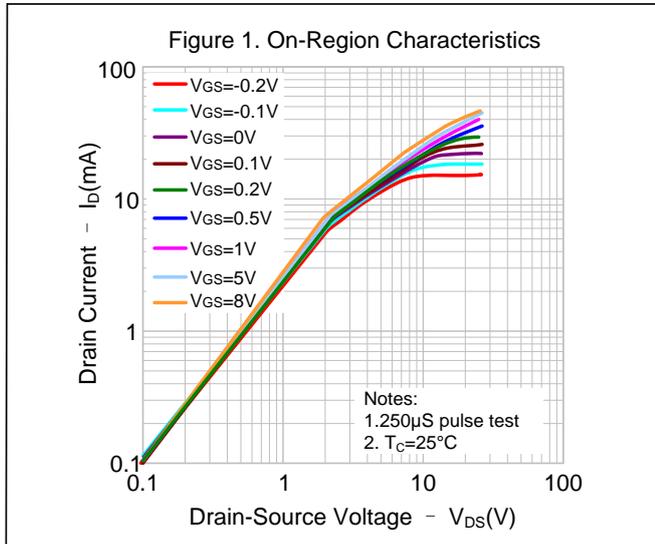
## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

| Characteristics           | Symbol   | Test conditions               | Min. | Typ. | Max. | Unit |
|---------------------------|----------|-------------------------------|------|------|------|------|
| Continuous Source Current | $I_S$    | Integral Reverse P-N Junction | --   | --   | 0.03 | A    |
| Pulsed Source Current     | $I_{SM}$ | Diode in the MOSFET           | --   | --   | 0.12 |      |
| Diode Forward Voltage     | $V_{SD}$ | $I_F=16mA, V_{GS}=-5V$        | --   | --   | 1.2  | V    |
| Reverse Recovery Time     | $T_{rr}$ | $I_F=0.01A, V_R=300V,$        | --   | --   | 367  | ns   |
| Reverse Recovery Charge   | $Q_{rr}$ | $dI_F/dt=100A/\mu s$ (Note 2) | --   | --   | 963  | nC   |

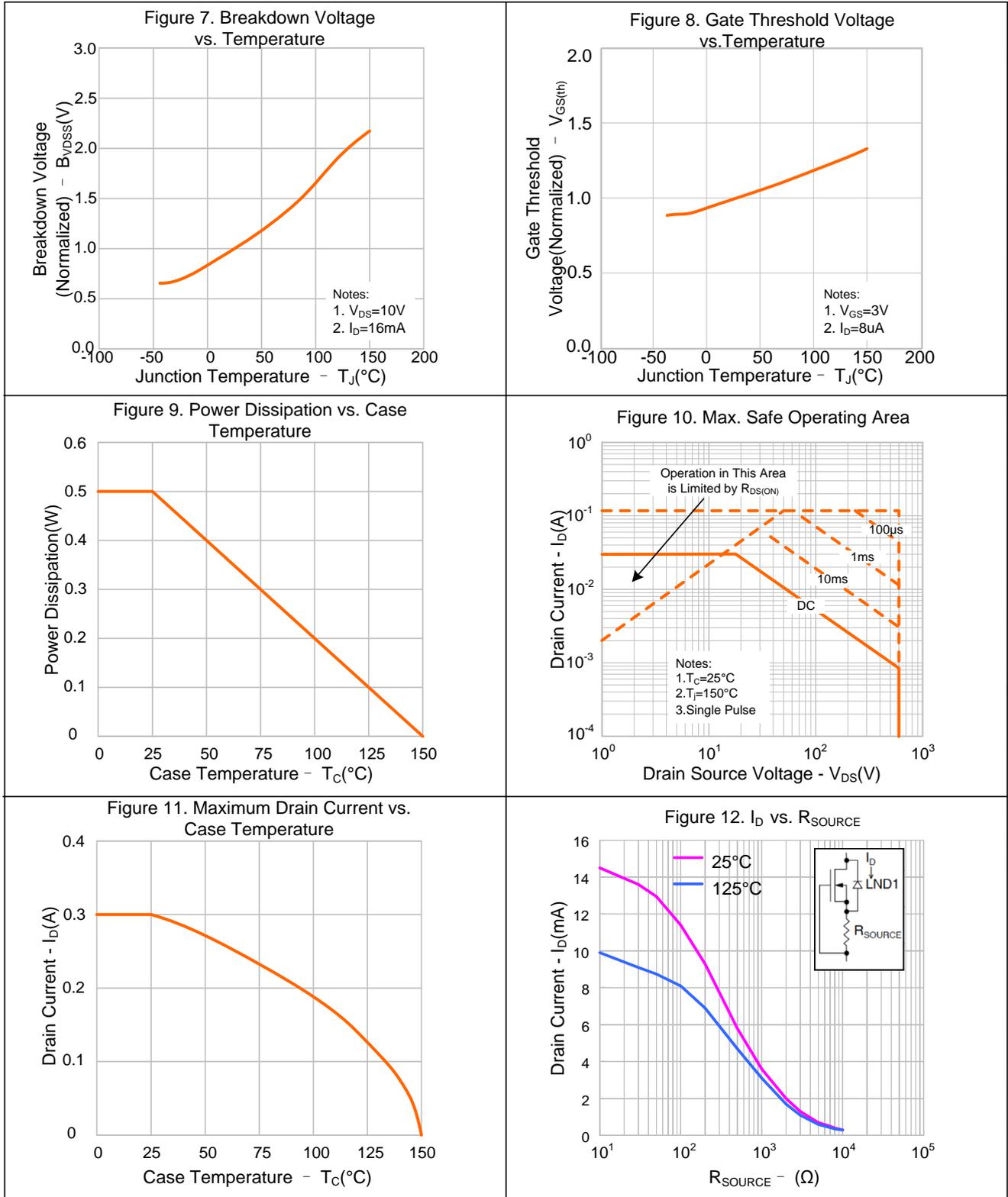
**Notes:**

1. Pulse Test: Pulse width  $\leq$  pulse Test: cycle Tes
2. Essentially independent of operating temperature.

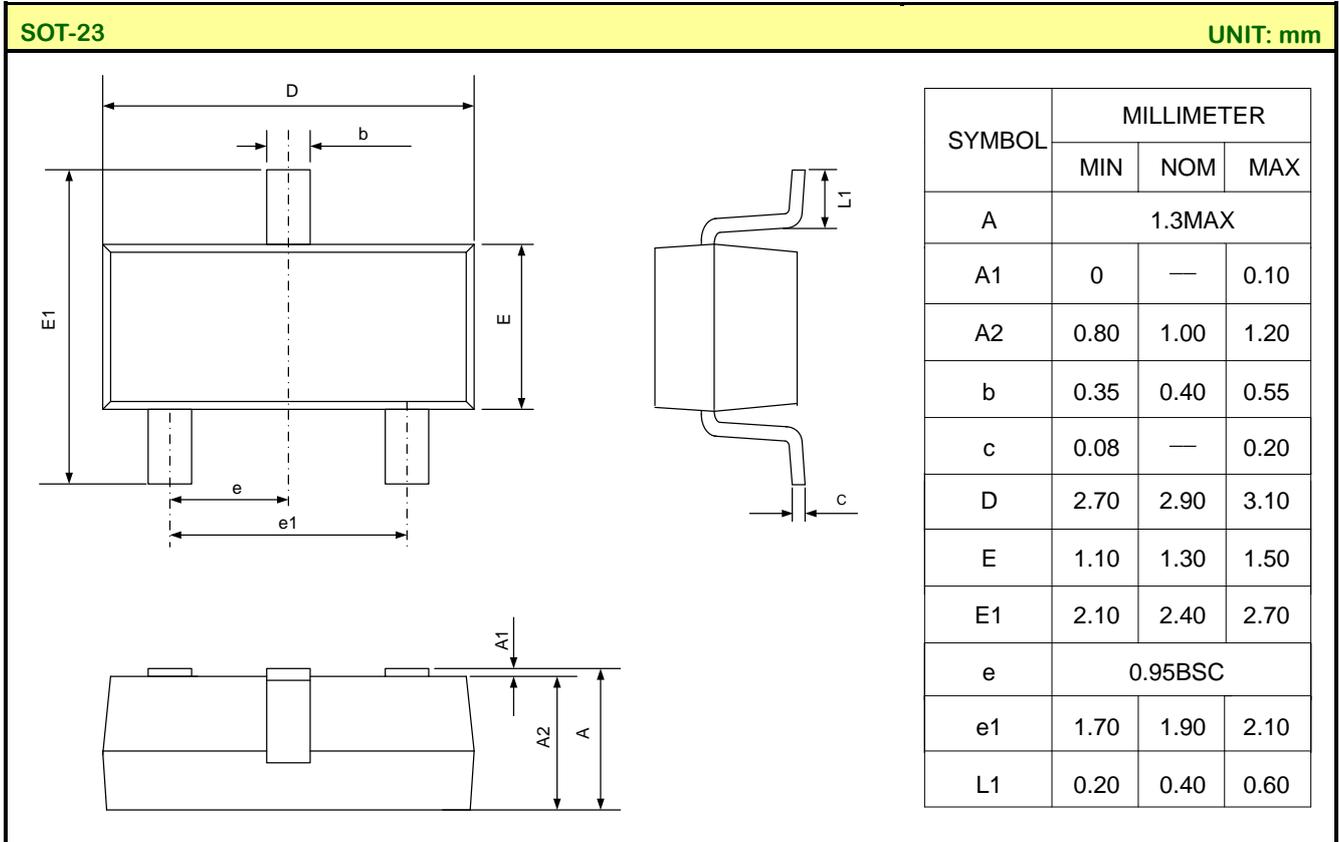
**TYPICAL CHARACTERISTICS**



**TYPICAL CHARACTERISTICS (CONTINUED)**



**PACKAGE OUTLINE**



**Important notice :**

- The instructions are subject to change without notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- Our products are consumer electronic products, and / or civil electronic products.
- When using our products, please do not exceed the maximum rating of the products, otherwise the reliability of the whole machine will be affected. There is a certain possibility of failure or malfunction of any semiconductor product under specific conditions. The buyer is responsible for complying with safety standards and taking safety measures when using our products for system design, sample and whole machine manufacturing, so as to avoid potential failure risk that may cause personal injury or property loss.
- It is strongly recommended to identify the trademark when buying our products. Please contact us if there is any question.
- When exporting, using and reselling our products, buyer must comply with the international export control laws and regulations of China, the United States, the United Kingdom, the European Union and other countries & regions.
- Product promotion is endless, our company will wholeheartedly provide customers with better products!
- Website: <http://www.silan.com.cn>

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Rev.: 1.7

Revision History:

1. Modify individual parameters
2. Add the figure 12

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Rev.: 1.6

Revision History:

1. Modify individual parameters
2. Modify TYPICAL CHARACTERISTICS

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Rev.: 1.5

Revision History:

1. Modify Tc of Drain Current
2. Modify BV<sub>DSS</sub>

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Rev.: 1.4

Revision History:

1. Modify the curve

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Rev.: 1.3

Revision History:

1. Modify Typical Characteristics

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Rev.: 1.2

Revision History:

1. Modify “ELECTRICAL CHARACTERISTICS”

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Rev.: 1.1

Revision History:

1. Update figure 3 and figure 7

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Rev.: 1.0

Revision History:

1. Initial release
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