

8A, 700V N-CHANNEL MOSFET

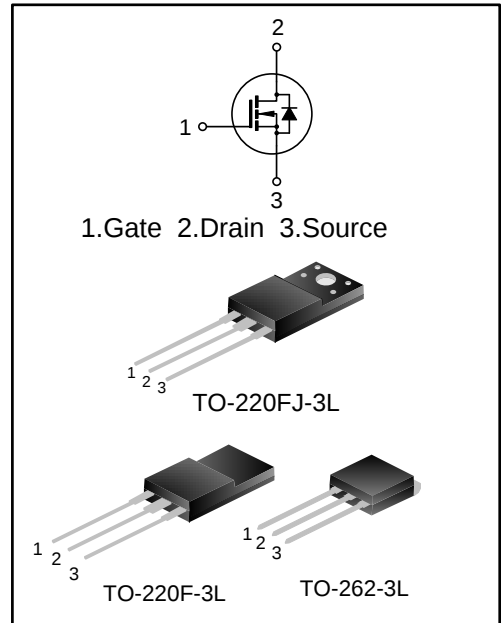
GENERAL DESCRIPTION

SVF8N70F/K/FJ is an N-channel enhancement mode power MOS field effect transistor which is produced using Silan proprietary F-Cell™ high-voltage planar VDMOS technology. The improved process and cell structure have been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are widely used in AC-DC power suppliers, DC-DC converters and H-bridge PWM motor drivers.

FEATURES

- ♦ 8A, 700V, $R_{DS(on)(typ.)}=0.95\Omega@V_{GS}=10V$
- ♦ Low gate charge
- ♦ Low C_{rss}
- ♦ Fast switching
- ♦ Improved dv/dt capability



ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SVF8N70F	TO-220F-3L	SVF8N70F	Pb free	Tube
SVF8N70K	TO-262-3L	SVF8N70K	Pb free	Tube
SVF8N70FJ	TO-220FJ-3L	SVF8N70FJ	Halogen free	Tube

ABSOLUTE MAXIMUM RATINGS (T_c=25°C, UNLESS OTHERWISE NOTED)

Characteristics		Symbol	Ratings		Unit
			SVF8N70F/FJ	SVF8N70K	
Drain-Source Voltage		V _{DS}	700		V
Gate-Source Voltage		V _{GS}	±30		V
Drain Current	T _C =25°C	I _D	8.0		A
	T _C =100°C		5.1		
Drain Current Pulsed		I _{DM}	32.0		A
Power Dissipation(T _C =25°C) - Derate above 25°C		P _D	52	140	W
			0.42	1.12	W/°C
Single Pulsed Avalanche Energy (Note 1)		E _{AS}	632		mJ
Operation Junction Temperature Rating		T _J	-55~+150		°C
Storage Temperature Rating		T _{stg}	-55~+150		°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	Ratings		Unit
		SVF8N70F/FJ	SVF8N70K	
Thermal Resistance, Junction-to-Case	R _{θJC}	2.40	0.89	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62.5	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_c=25°C UNLESS OTHERWISE NOTED)

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain –Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	700	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =700V, V _{GS} =0V	--	--	10	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	--	--	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	2.0	--	4.0	V
On State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =4.0A	--	0.95	1.2	Ω
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	--	1357	--	pF
Output Capacitance	C _{oss}		--	124	--	
Reverse Transfer Capacitance	C _{rss}		--	3.4	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} =350V, I _D =8.0A, R _G =24Ω (Note 2,3)	--	26	--	ns
Turn-on Rise Time	t _r		--	40	--	
Turn-off Delay Time	t _{d(off)}		--	87	--	
Turn-off Fall Time	t _f		--	43	--	
Total Gate Charge	Q _g	V _{DS} =560V, I _D =8.0A, V _{GS} =10V (Note 2,3)	--	28	--	nC
Gate-Source Charge	Q _{gs}		--	10	--	
Gate-Drain Charge	Q _{gd}		--	9.1	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction	--	--	8.0	A
Pulsed Source Current	I_{SM}	Diode in the MOSFET	--	--	32.0	
Diode Forward Voltage	V_{SD}	$I_S=8.0A$, $V_{GS}=0V$	--	--	1.4	V
Reverse Recovery Time	T_{rr}	$I_S=8.0A$, $V_{GS}=0V$,	--	515	--	ns
Reverse Recovery Charge	Q_{rr}	$di_F/dt=100A/\mu s$ (Note 2)	--	4.2	--	μC

Notes:

1. $L=30mH$, $I_{AS}=5.80A$, $V_{DD}=140V$, $R_G=25\Omega$, starting $T_J=25^\circ C$;
2. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$;
3. Essentially independent of operating temperature.

TYPICAL CHARACTERISTICS

Figure 1. On-Region Characteristics

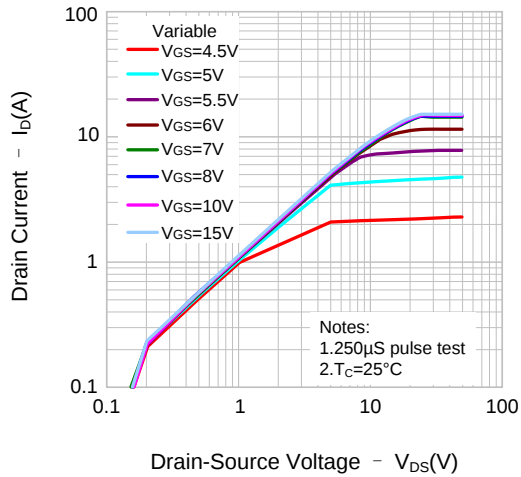


Figure 2. Transfer Characteristics

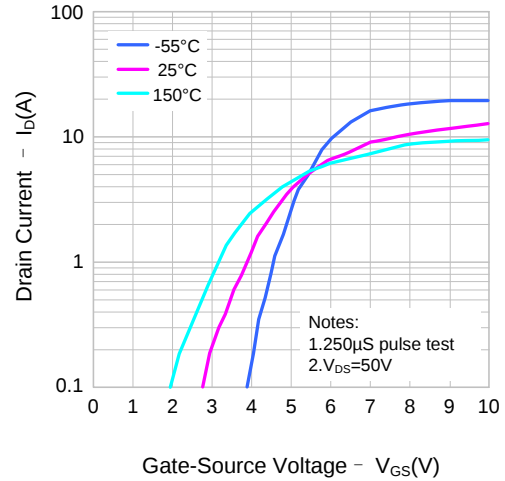


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

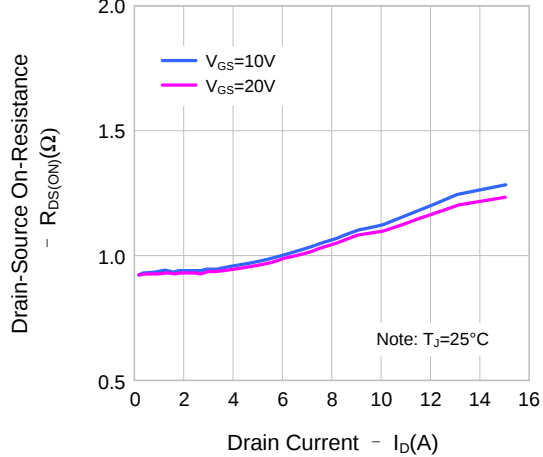


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

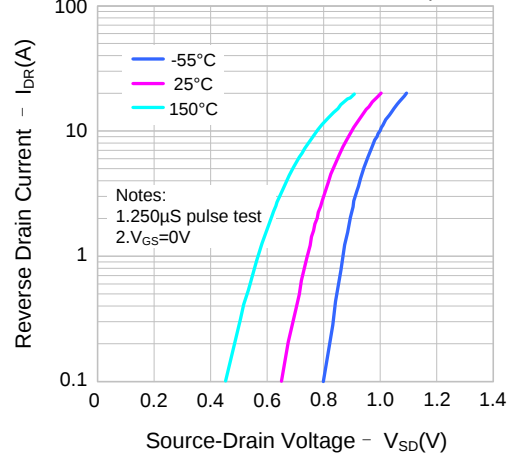


Figure 5. Capacitance Characteristics

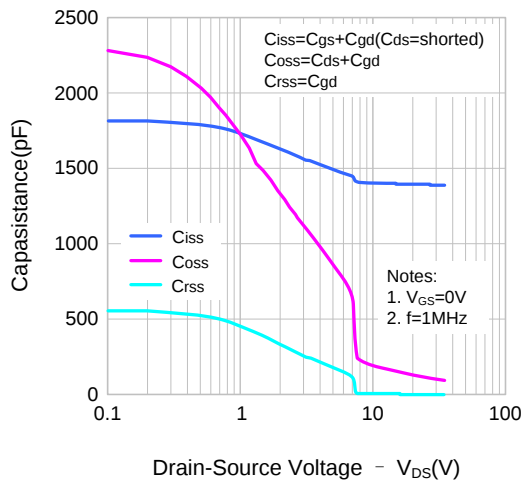
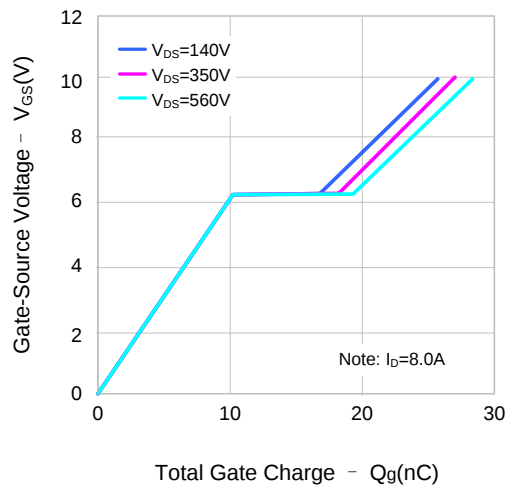


Figure 6. Gate Charge Characteristics



TYPICAL CHARACTERISTICS(CONTINUED)

Figure 7. Breakdown Voltage Variation vs. Temperature

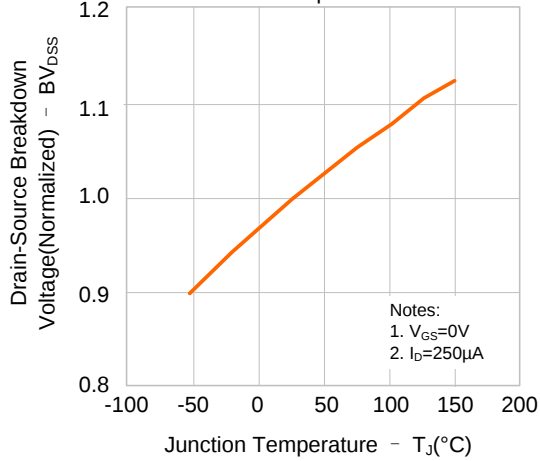


Figure 8. On-resistance Variation vs. Temperature

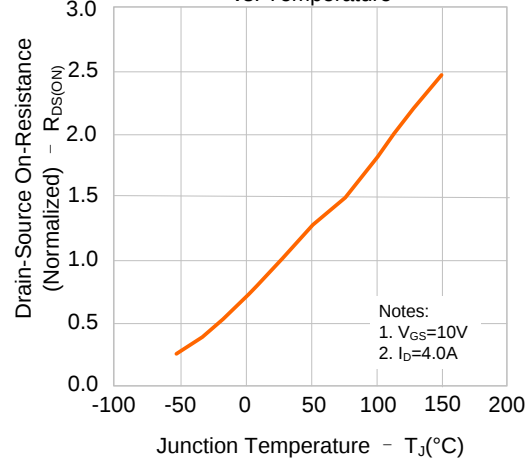


Figure 9-1. Max. Safe Operating Area(SVF8N70F)

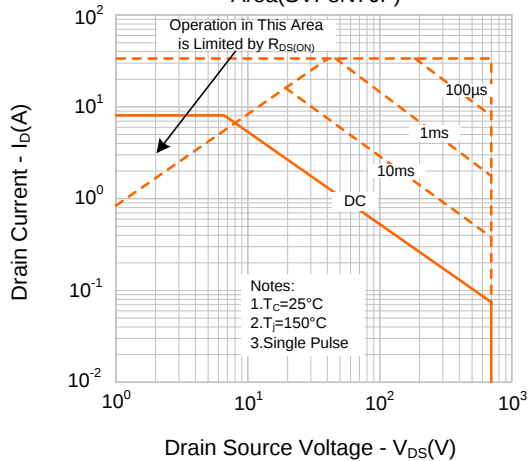


Figure 9-2. Max. Safe Operating Area(SVF8N70K)

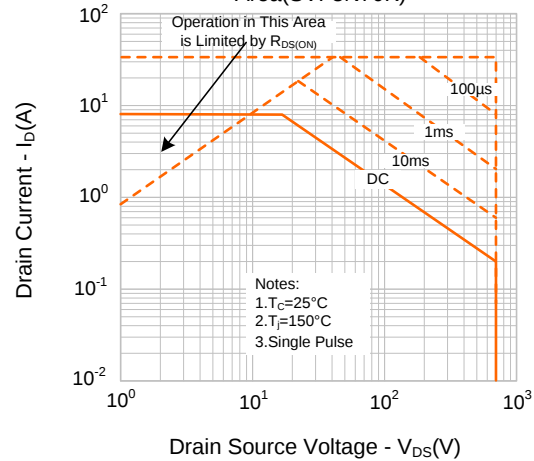
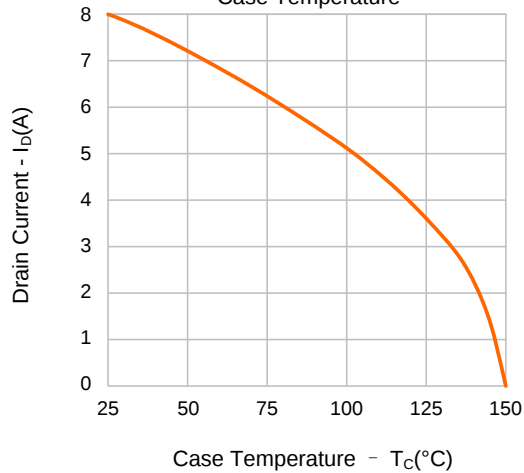
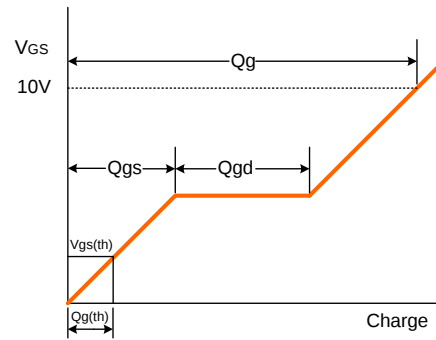
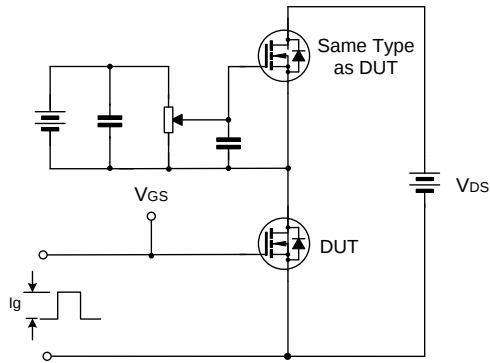


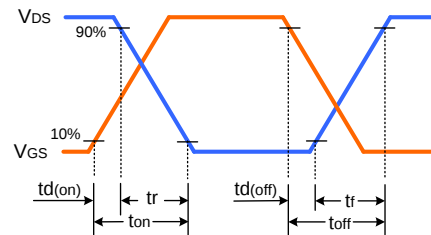
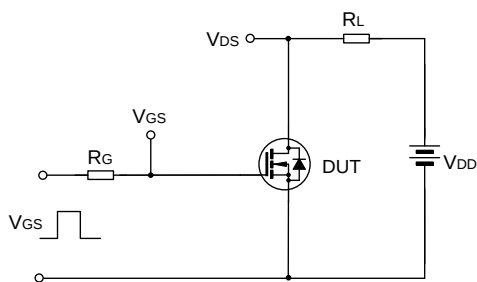
Figure 10. Maximum Drain Current vs. Case Temperature



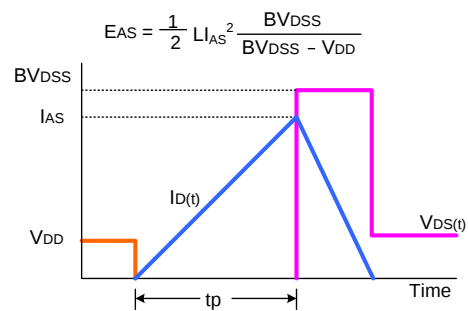
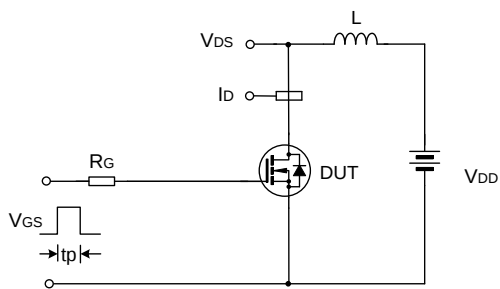
TYPICAL TEST CIRCUIT



Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

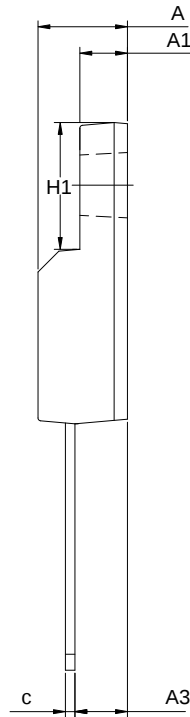
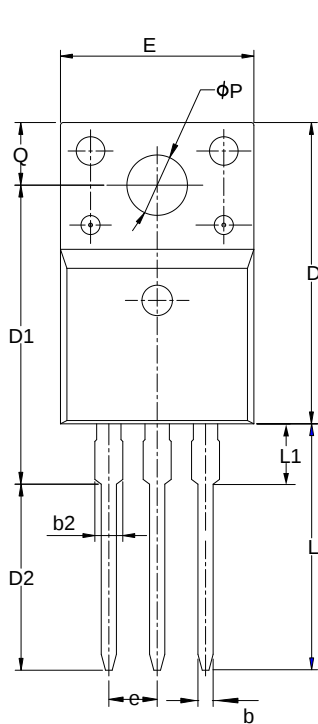


Unclamped Inductive Switching Test Circuit & Waveform

PACKAGE OUTLINE

TO-220F-3L

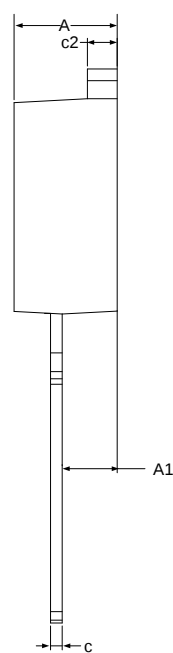
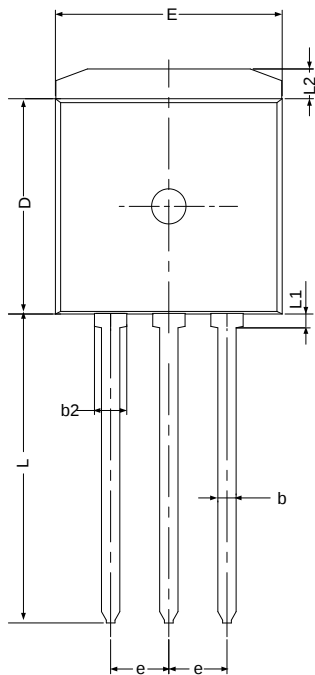
UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BSC		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	—	—	3.50
ϕP	3.00	3.18	3.40
Q	3.05	3.30	3.55

TO-262-3L

UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	2.20	—	2.92
b	0.71	0.80	0.90
b2	1.20	—	1.50
c	0.34	—	0.65
c2	1.22	1.30	1.35
D	8.38	—	9.30
E	9.80	10.16	10.54
e	2.54 BSC		
L	12.80	—	14.10
L1	—	—	0.75
L2	1.12	—	1.42

PACKAGE OUTLINE(CONTINUED)

TO-220FJ-3L

UNIT: mm

The image shows two views of the TO-220FJ-3L package. The top view (left) shows a rectangular body with three leads. Dimensions include: E (width), Q (distance from top edge to lead base), D1 (body width), D2 (lead length), L (total length), L1 (lead length from body), b2 (lead width), e (lead pitch), b (lead width), and ϕP (lead diameter). The side view (right) shows the package profile with dimensions: A (total height), A1 (lead height), H1 (lead height from body), and A3 (lead width).

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.55	0.70	0.85
b2	—	—	1.29
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	13.97	14.47	14.97
D2	10.58	11.08	11.58
E	9.73	10.16	10.36
e	2.54BSC		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	—	—	2.00
ϕP	3.00	3.18	3.40
Q	3.05	3.30	3.55

Important notice :

- The instructions are subject to change without notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- Our products are consumer electronic products, and / or civil electronic products.
- When using our products, please do not exceed the maximum rating of the products, otherwise the reliability of the whole machine will be affected. There is a certain possibility of failure or malfunction of any semiconductor product under specific conditions. The buyer is responsible for complying with safety standards and taking safety measures when using our products for system design, sample and whole machine manufacturing, so as to avoid potential failure risk that may cause personal injury or property loss.
- It is strongly recommended to identify the trademark when buying our products. Please contact us if there is any question.
- When exporting, using and reselling our products, buyer must comply with the international export control laws and regulations of China, the United States, the United Kingdom, the European Union and other countries & regions.
- Product promotion is endless, our company will wholeheartedly provide customers with better products!
- Website: <http://www.silan.com.cn>

Part No.:	SVF8N70F/K/FJ	Document Type:	Datasheet
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Rev.: 2.2

Revision History:

1. Deleted NOMENCLATURE
 2. Modify Important notice
 3. Modify the Hazardous Substance Control of SVF8N70K
-

Rev.: 2.1

Revision History:

1. Modify the ordering information
 2. Update TYPICAL TEST CIRCUIT
 3. Update Important notice
-

Rev.: 2.0

Revision History:

1. Modify the Electrical characteristics
 2. Update Fig 6
-

Rev.: 1.9

Revision History:

1. Modify the Value of C_{RSS}
 2. Modify the package outline of TO-262-3L
 3. Modify Ordering Information
-

Rev.: 1.8

Revision History:

1. Modify the package information of TO-220F-3L
 2. Modify the package information of TO-220FJ-3L
-

Rev.: 1.7

Revision History:

1. Modify the thermal characteristics
-

Rev.: 1.6

Revision History:

1. Add the package of TO-262-3L
-

Rev.: 1.5

Revision History:

1. Modify the ordering information
-

Rev.: 1.4

Revision History:

1. Change the schematic diagram of MOS
-

Rev.: 1.3

Revision History:

1. Modify the parameters and "TYPICAL CHARACTERISTICS"
-

Rev.: 1.2

Revision History:

1. Add the halogen free information of SVF8N70F
-

Rev.: 1.1

Revision History:

1. Modify "PACKAGE OUTLINE"
-

Rev.: 1.0

Revision History:

1. Original
-