

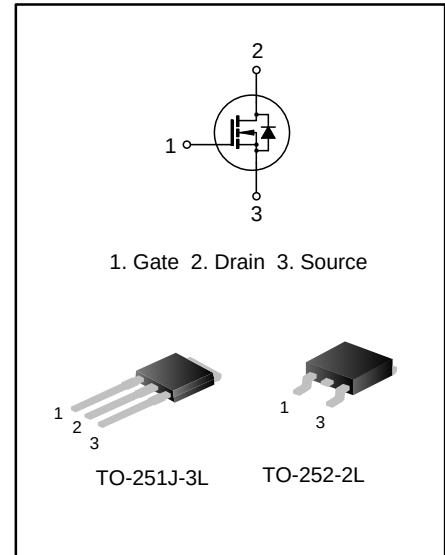
9A, 650V N-CHANNEL MOSFET

GENERAL DESCRIPTION

SVF65R950CMJ/D is an N-channel enhancement mode power MOS field effect transistor which is produced using Silan proprietary F-Cell™ high-voltage planar VDMOS technology. The improved process and cell structure have been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are widely used in AC-DC power supplies, DC-DC converters and H-bridge PWM motor drivers.

FEATURES

- 9A, 650V, $R_{DS(on)(typ.)}=0.85\Omega@V_{GS}=10V$
- Ultra low gate charge
- Low C_{rss}
- Fast switching
- Improved dv/dt capability



ORDERING INFORMATION

Part No.	Package	Marking	Hazardous substance control	Packing Type
SVF65R950CMJ	TO-251J-3L	65R950	Halogen free	Tube
SVF65R950CDTR	TO-252-2L	65R950	Halogen free	Tape&Reel

ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$ UNLESS OTHERWISE NOTED)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	V
Drain Current	I_D	$T_C=25^{\circ}\text{C}$	A
		$T_C=100^{\circ}\text{C}$	
Drain Current Pulsed	I_{DM}	36	A
Power Dissipation($T_C=25^{\circ}\text{C}$) -Derate above 25°C	P_D	148	W
		1.18	W/ $^{\circ}\text{C}$
Single Pulsed Avalanche Energy (Note 1)	E_{AS}	565	mJ
Operation Junction Temperature Range	T_J	$-55\sim+150$	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	$-55\sim+150$	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

Characteristics	Symbol	Ratings	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.82	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.0	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$ UNLESS OTHERWISE NOTED)

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain -Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650	--	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V$	--	--	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	--	--	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2.0	--	4.0	V
Static Drain-Source On State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=4.5A$	--	0.85	0.98	Ω
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{MHz}$	--	1038	--	pF
Output Capacitance	C_{oss}		--	120	--	
Reverse Transfer Capacitance	C_{rss}		--	11	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=350V, V_{GS}=10V, R_G=24\Omega, I_D=9A$ (Note 2,3)	--	18	--	ns
Turn-on Rise Time	t_r		--	35	--	
Turn-off Delay Time	$t_{d(off)}$		--	74	--	
Turn-off Fall Time	t_f		--	39	--	
Total Gate Charge	Q_g	$V_{DD}=560V, V_{GS}=10V, I_D=9A$ (Note 2,3)	--	27	--	nC
Gate-Source Charge	Q_{gs}		--	7.7	--	
Gate-Drain Charge	Q_{gd}		--	12	--	

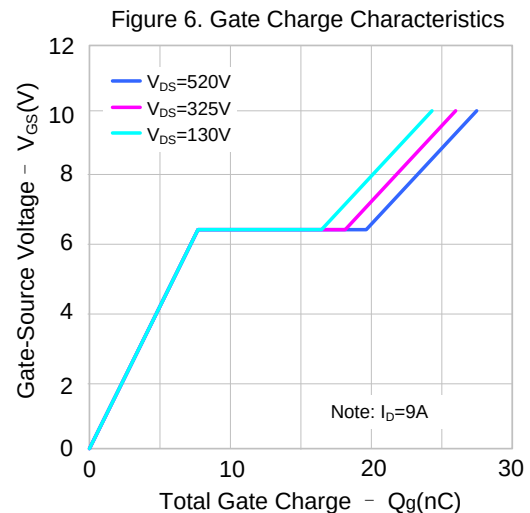
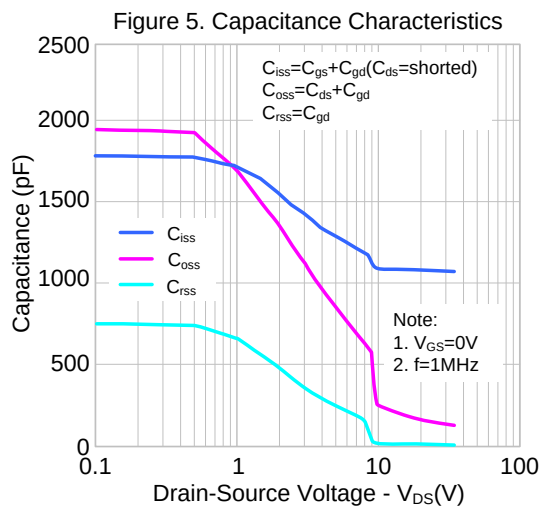
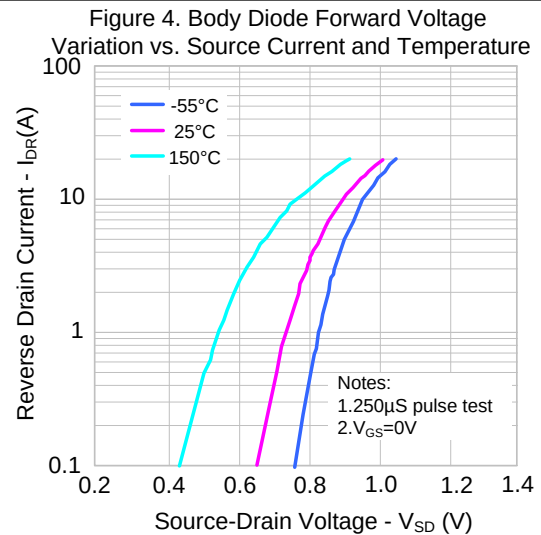
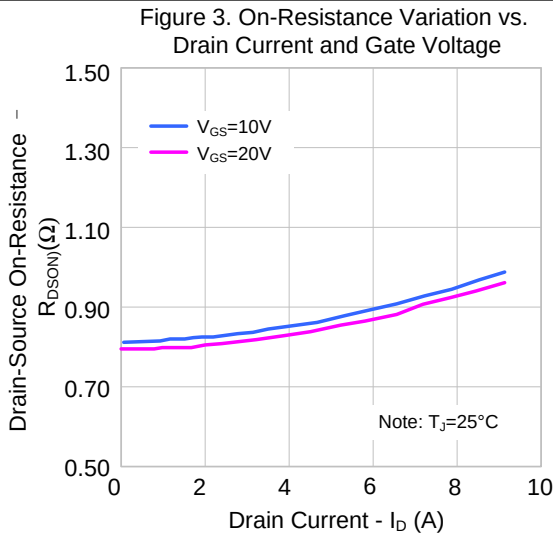
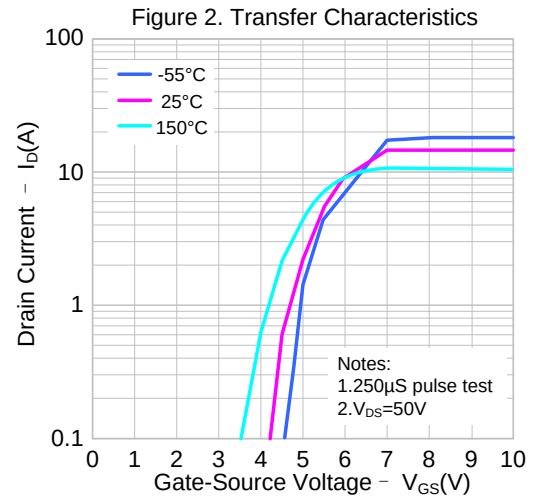
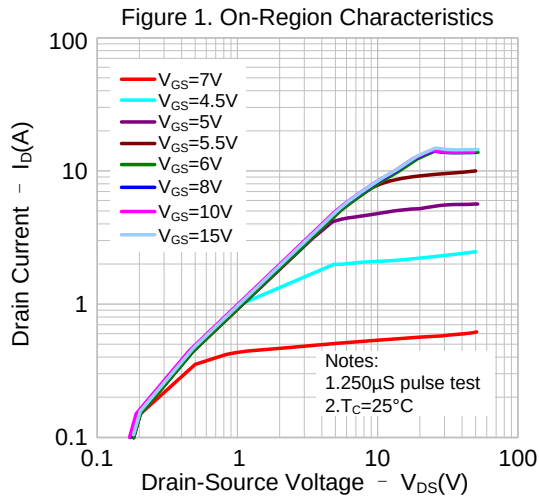
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	9	A
Pulsed Source Current	I_{SM}		--	--	36	
Diode Forward Voltage	V_{SD}	$I_S=9A, V_{GS}=0V$	--	--	1.4	V
Reverse Recovery Time	T_{rr}	$I_S=9A, V_{GS}=0V, dI_F/dt=100A/\mu s$	--	500	--	ns
Reverse Recovery Charge	Q_{rr}		--	4.2	--	μC

Notes:

1. $L=30\text{mH}, I_{AS}=5.8A, V_{DD}=100V, R_G=25\Omega$, starting temperature $T_J=25^{\circ}\text{C}$;
2. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycles $\leq 2\%$;
3. Essentially independent of operating temperature.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (CONTINUED)

Figure 7. Breakdown Voltage Variation vs. Temperature

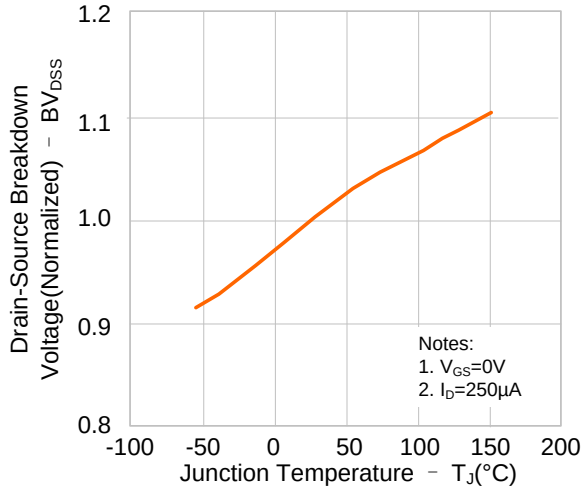


Figure 8. On-resistance Variation vs. Temperature

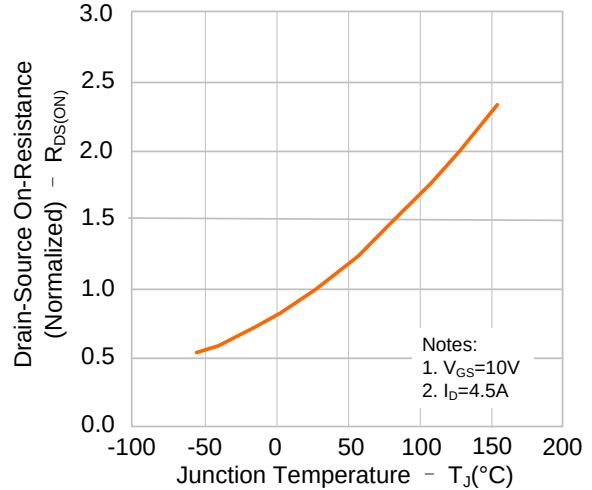


Figure 9. Max. Safe Operating Area

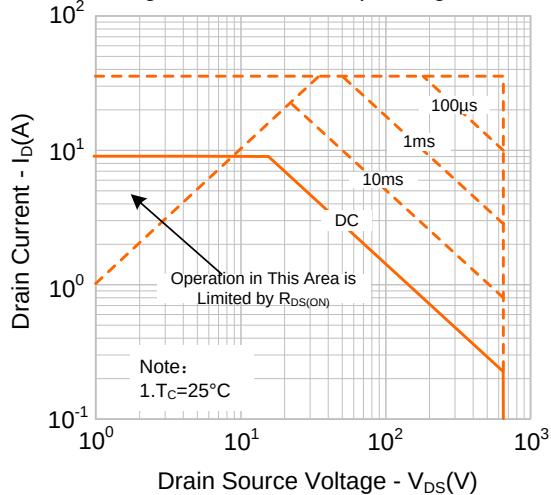
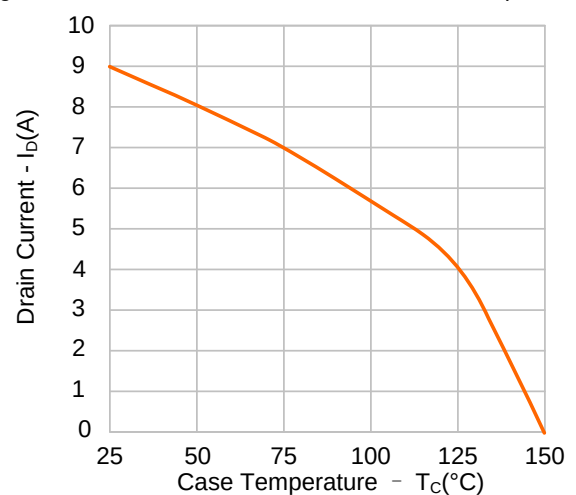
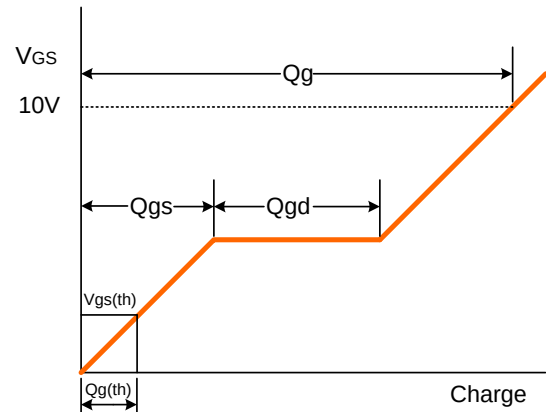
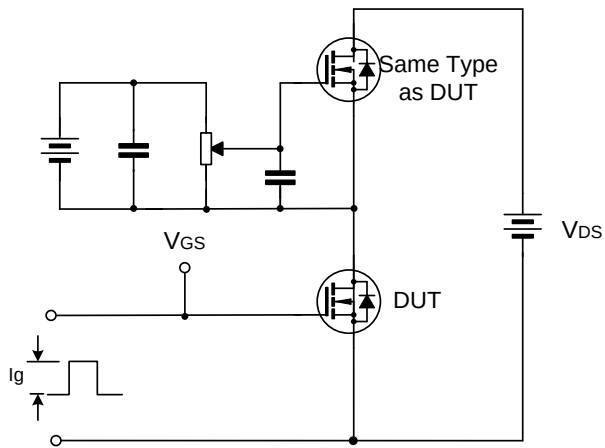


Figure 10. Maximum Drain Current vs. Case Temperature

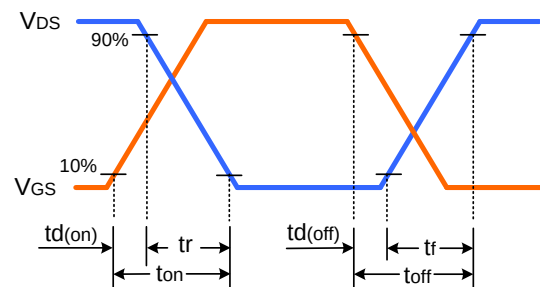
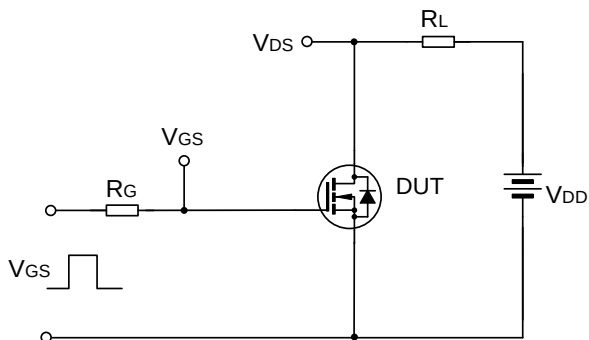


TYPICAL TEST CIRCUIT

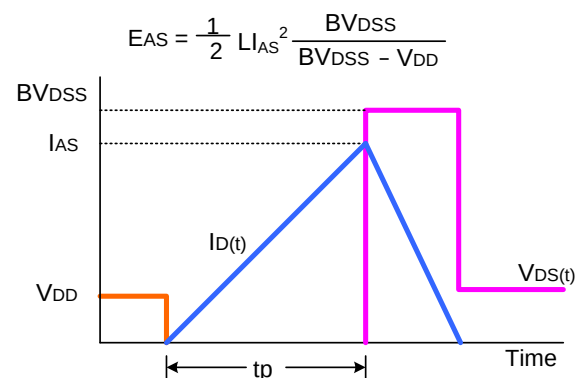
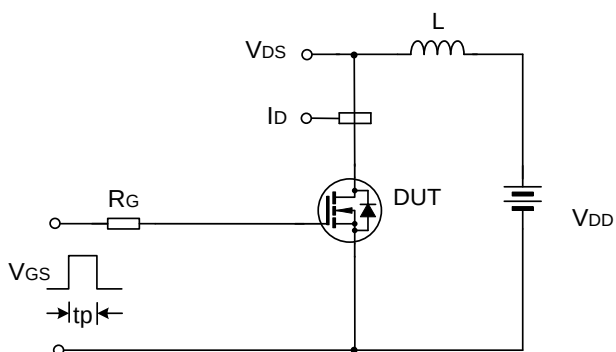
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



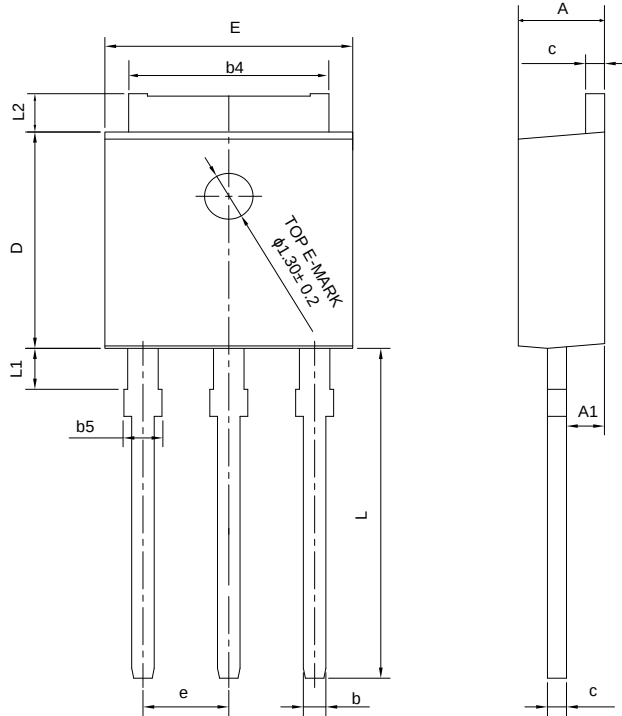
Unclamped Inductive Switching Test Circuit & Waveform



PACKAGE OUTLINE

TO-251J-3L

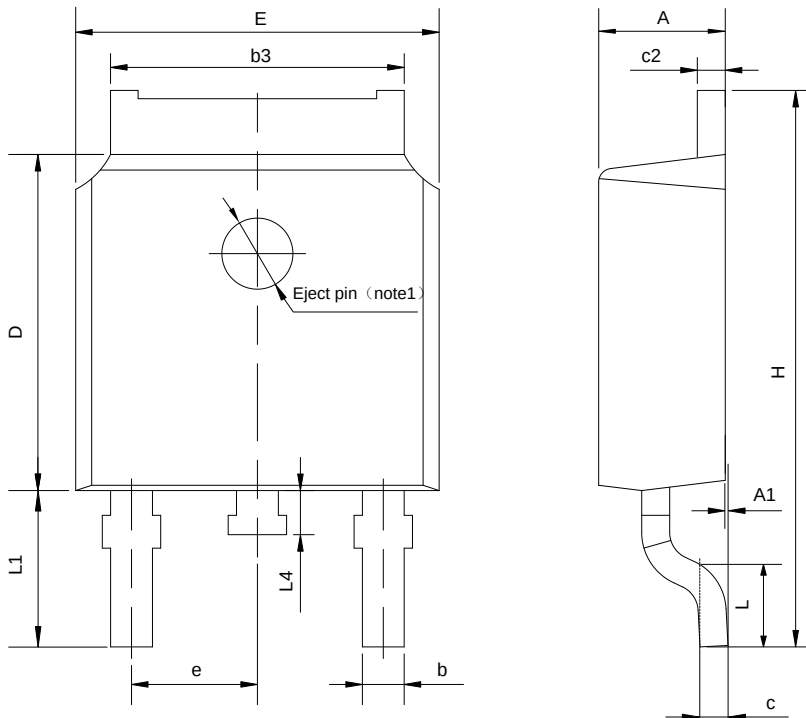
UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.18	2.30	2.39
A1	0.89	1.00	1.14
b	0.56	—	0.89
b4	4.95	5.33	5.46
b5	—	—	1.05
c	0.46	—	0.61
D	5.97	6.10	6.27
E	6.35	6.60	6.73
e	2.29 BCS		
L	8.89	9.30	9.65
L1	0.95	—	1.50
L2	0.89	—	1.27

TO-252-2L

UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.10	2.30	2.50
A1	0	—	0.127
b	0.66	0.76	0.89
b3	5.10	5.33	5.46
c	0.45	—	0.65
c2	0.45	—	0.65
D	5.80	6.10	6.40
E	6.30	6.60	6.90
e	2.30TYP		
H	9.60	10.10	10.60
L	1.40	1.50	1.70
L1	2.90REF		
L4	0.60	0.80	1.00

NOTE1 : There are two conditions for this position:has an eject pin or has no eject pin.

Important notice:

1. The instructions are subject to change without notice!
2. Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current. Please read the instructions carefully before using our products, including the circuit operation precautions.
3. Our products are consumer electronic products or the other civil electronic products.
4. When using our products, please do not exceed the maximum rating of the products, otherwise the reliability of the whole machine will be affected. There is a certain possibility of failure or malfunction of any semiconductor product under specific conditions. The buyer is responsible for complying with safety standards and taking safety measures when using our products for system design, sample and whole machine manufacturing, so as to avoid potential failure risk that may cause personal injury or property loss.
5. It is strongly recommended to identify the trademark when buying our products. Please contact us if there is any question.
6. Product promotion is endless, our company will wholeheartedly provide customers with better products!
7. Website: <http://www.silan.com.cn>

Part No.:	SVF65R950CMJ(D)	Document Type:	Datasheet
Copyright:	HANGZHOU SILAN MICROELECTRONICS CO.,LTD	Website:	http://www.silan.com.cn

Rev.: 1.4

Revision History:

1. Update Turn-on/off time

Rev.: 1.3

Revision History:

1. Update the specification template
2. Update the main notes

Rev.: 1.2

Revision History:

1. Update the package outline of TO-251J-3L

Rev.: 1.1

Revision History:

1. Modify the Typical Characteristics

Rev.: 1.0

Revision History:

1. First release
-