

## 6A, 700V N-CHANNEL MOSFET

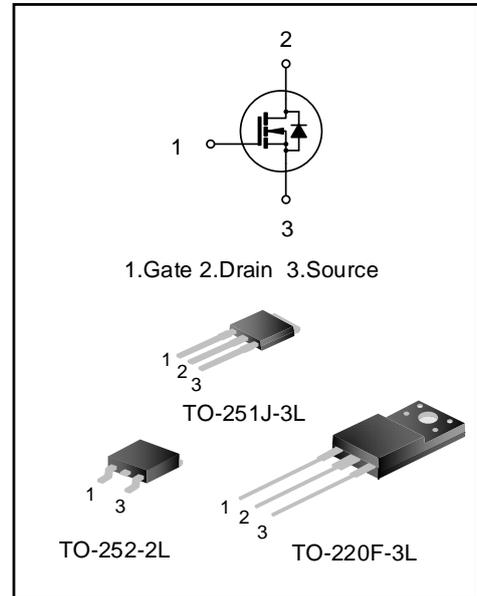
### DESCRIPTION

SVF6N70AD(F)(MJ) is an N-channel enhancement mode power MOS field effect transistor which is produced using Silan proprietary F-Cell™ high-voltage planar VDMOS technology. The improved process and cell structure have been especially tailored to minimize on-state resistance, provide superior switching performance, and high avalanche breakdown resistance.

These devices are widely used in AC-DC power supplies, DC-DC converters and H-bridge PWM motor drivers.

### FEATURES

- ◆ 6A, 700V,  $R_{DS(on)(typ.)}=1.6\Omega@V_{GS}=10V$
- ◆ Low gate charge
- ◆ Low  $C_{rss}$
- ◆ Fast switching
- ◆ Improved  $dv/dt$  capability
- ◆ 100% avalanche tested
- ◆ Pb-free lead plating
- ◆ RoHS compliant



### KEY PERFORMANCE PARAMETERS

Characteristics	Ratings	Unit
$V_{DS}$	700	V
$V_{GS(th)}$	2.0~4.0	V
$R_{DS(on), max.}$	1.8	$\Omega$
$I_{D,pulse}$	24	A
$Q_{g,typ.}$	20	nC

### ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SVF6N70ADTR	TO-252-2L	6N70AD	Halogen free	Tape & Reel
SVF6N70AF	TO-220F-3L	SVF6N70AF	pb free	Tube
SVF6N70AMJ	TO-251J-3L	SVF6N70A	Halogen free	Tube

**ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, T<sub>J</sub>=25°C)**

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-source Voltage	V <sub>DS</sub>	--	700	--	--	V
Gate-source Voltage	V <sub>GS</sub>	--	-30	--	30	V
Drain Current	I <sub>D</sub>	T <sub>C</sub> =25°C	--	--	6.0	A
		T <sub>C</sub> =100°C	--	--	3.8	A
Drain Current Pulsed (Note 1)	I <sub>DM</sub>	T <sub>C</sub> =25°C	--	--	24	A
Power Dissipation (TO-252-2L) (TO-251J-3L) (Note 2)	P <sub>D</sub>	T <sub>C</sub> =25°C	--	--	119	W
Power Dissipation (TO-220F-3L) (Note 2)	P <sub>D</sub>	T <sub>C</sub> =25°C	--	--	43	W
Single Pulsed Avalanche Energy	E <sub>AS</sub>	L=10mH, V <sub>DD</sub> =100V, R <sub>G</sub> =25Ω, starting temperature T <sub>J</sub> =25°C	--	--	245	mJ
Single pulse avalanche current	I <sub>AS</sub>	--	--	--	6.6	A
Reverse Diode dv/dt	dv/dt	V <sub>DS</sub> =0~600V, I <sub>SD</sub> <=I <sub>S</sub> , T <sub>J</sub> =25°C	--	--	4.5	V/ns
MOS dv/dt Ruggedness	dv/dt	V <sub>DS</sub> =0~640V	--	--	50	V/ns
Operation Junction Temperature Range	T <sub>J</sub>	--	-55	--	150	°C
Storage Temperature Range	T <sub>stg</sub>	--	-55	--	150	°C
Continuous Diode Forward Current	I <sub>S</sub>	T <sub>C</sub> =25°C, integral reverse P-N junction diode in the MOSFET	--	--	6.0	A
Diode Pulse Current	I <sub>S,pulse</sub>		--	--	24	A
Maximum Diode Commutation Speed	di/dt	V <sub>DS</sub> =0~400V, I <sub>SD</sub> <= I <sub>S</sub> , T <sub>J</sub> =25°C	--	--	250	A/μs

**THERMAL CHARACTERISTICS**

Table 1. TO-252-2L/TO-251J-3L (SVF6N70AD/MJ)

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal Resistance, Junction-case, Bottom	R <sub>θJC</sub>	--	--	--	1.05	°C/W
Thermal Resistance, Junction-ambient	R <sub>θJA</sub>	--	--	--	62.0	°C/W
Soldering Temperature (SMD)	T <sub>sold</sub>	Reflow soldering: 10±1sec, 3times	--	--	260	°C
Soldering Temperature (in line)	T <sub>sold</sub>	15 <sub>-0</sub> <sup>+2</sup> sec, 1time	--	--	260	°C

Table 2. TO-220F-3L (SVF6N70AF)

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal Resistance, Junction-case, Bottom	$R_{\theta JC}$	--	--	--	2.91	°C/W
Thermal Resistance, Junction-ambient	$R_{\theta JA}$	--	--	--	62.5	°C/W
Soldering Temperature (in line)	Tsold	$15_{0}^{+2}$ sec, 1time	--	--	260	°C

**ELECTRICAL CHARACTERISTICS (UNLESS OTHERWISE NOTED, T<sub>J</sub>=25°C)**
**Static characteristics**

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	700	--	--	V
Drain-source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	--	--	1.0	μA
		V <sub>DS</sub> =700V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	--	3.0	--	μA
Gate-source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	2.0	--	4.0	V
Static Drain-source On State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.0A	--	1.6	1.8	Ω
Gate Resistance	R <sub>G</sub>	f=1MHz	--	3.9	--	Ω

**Dynamic characteristics**

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Input Capacitance	C <sub>iss</sub>	f=1MHz, V <sub>GS</sub> =0V, V <sub>DS</sub> =25V	--	711	--	pF
Output Capacitance	C <sub>oss</sub>		--	79	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	7.2	--	
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =350V, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω, I <sub>D</sub> =6.0A (Notes 3, 4)	--	14	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	29	--	
Turn-off Delay Time	t <sub>d(off)</sub>		--	49	--	
Turn-off Fall Time	t <sub>f</sub>		--	31	--	
Total Gate Charge	Q <sub>g</sub>	V <sub>DD</sub> =560V, V <sub>GS</sub> =10V, I <sub>D</sub> =6.0A (Notes 3, 4)	--	20	--	nC
Gate-source Charge	Q <sub>gs</sub>		--	5.1	--	
Gate-drain Charge	Q <sub>gd</sub>		--	8.7	--	
Gate-plateau Voltage	V <sub>plateau</sub>		--	6.2	--	V

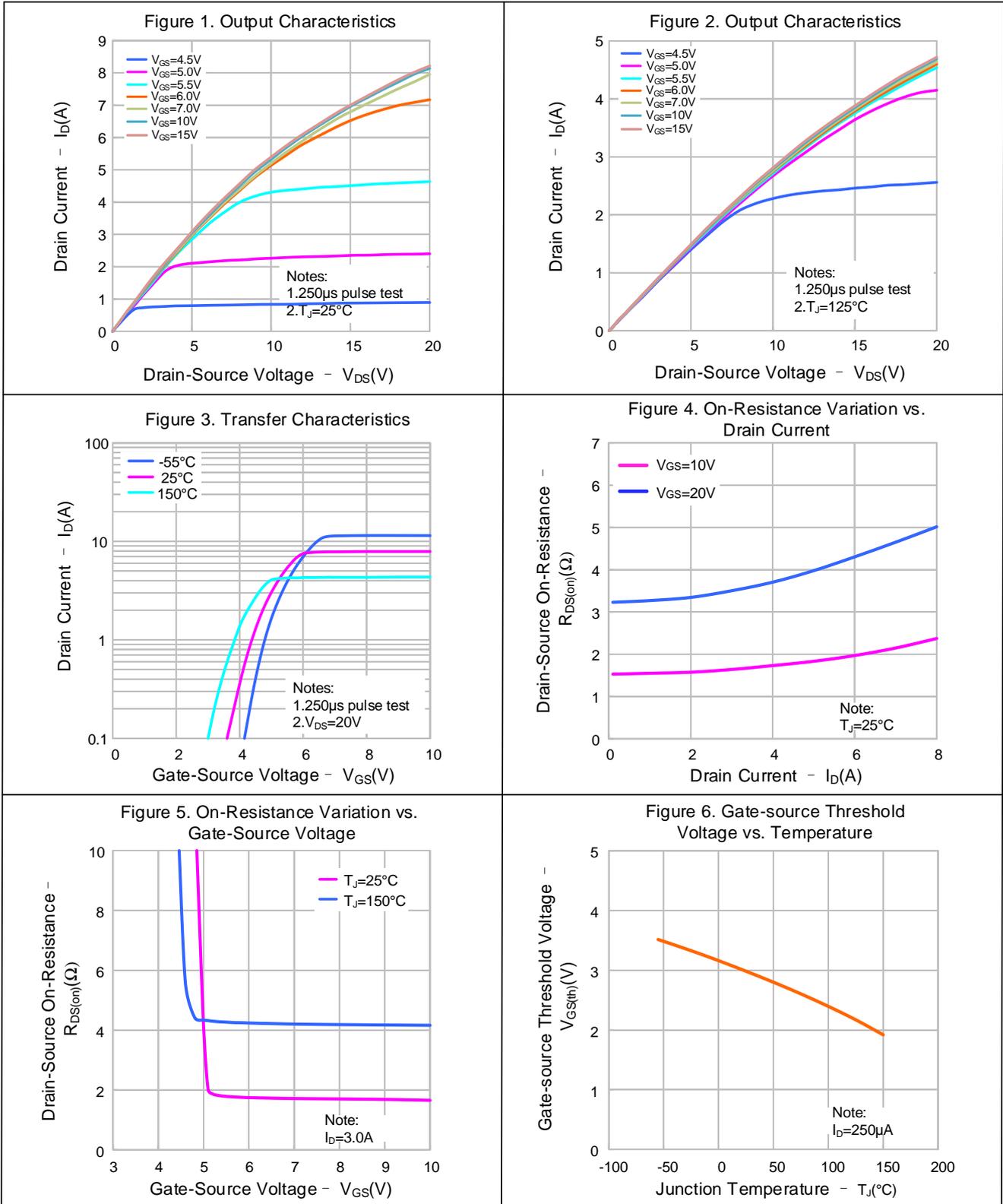
**Reverse diode characteristics**

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =6.0A, V <sub>GS</sub> =0V	--	--	1.4	V
Reverse Recovery Time	T <sub>rr</sub>	I <sub>S</sub> =6.0A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs (Note 3)	--	458	--	ns
Reverse Recovery Charge	Q <sub>rr</sub>		--	2.9	--	μC
Reverse Recovery Peak Current	I <sub>rrm</sub>		--	14	--	A

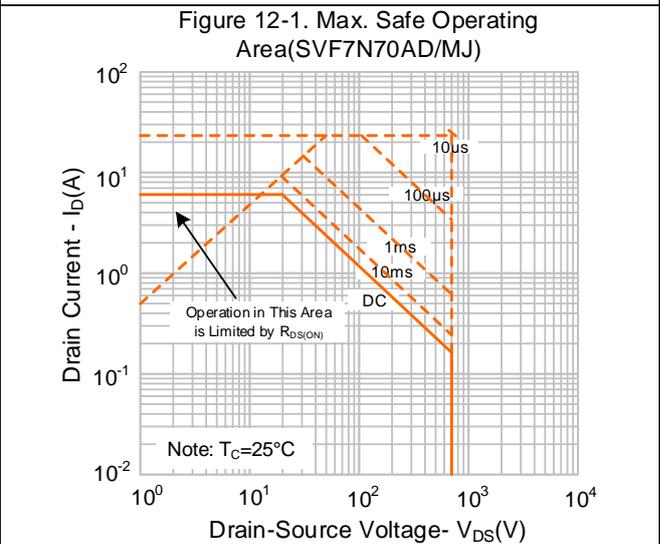
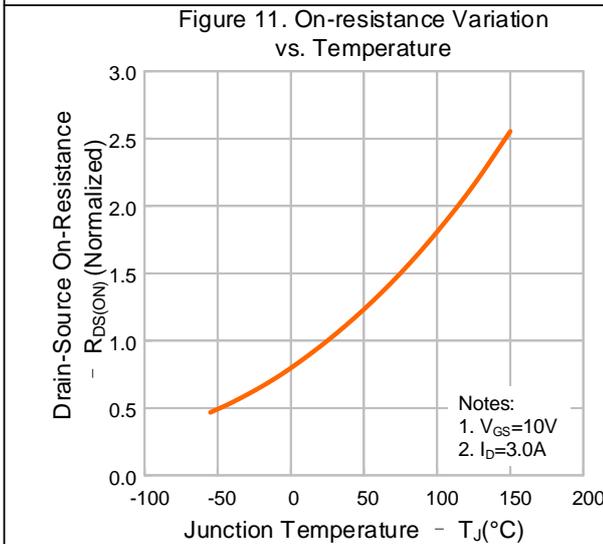
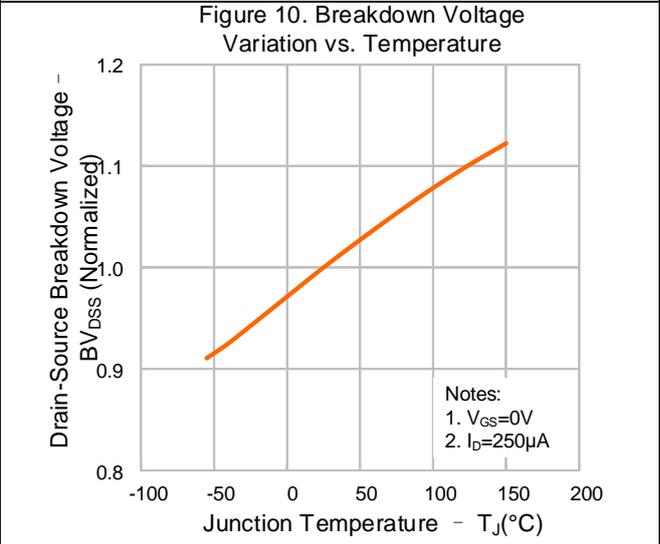
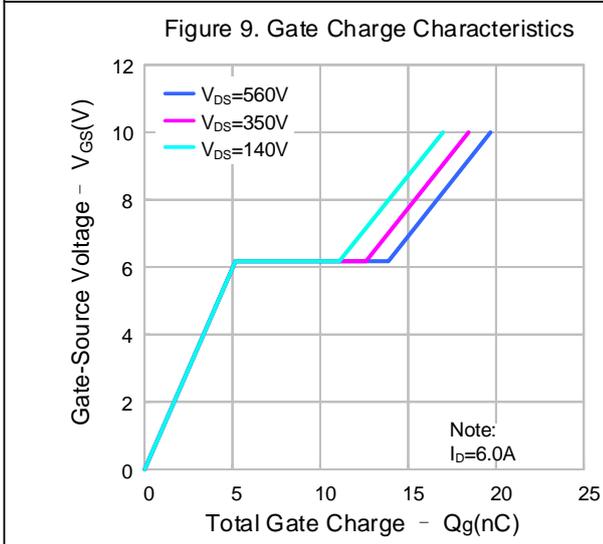
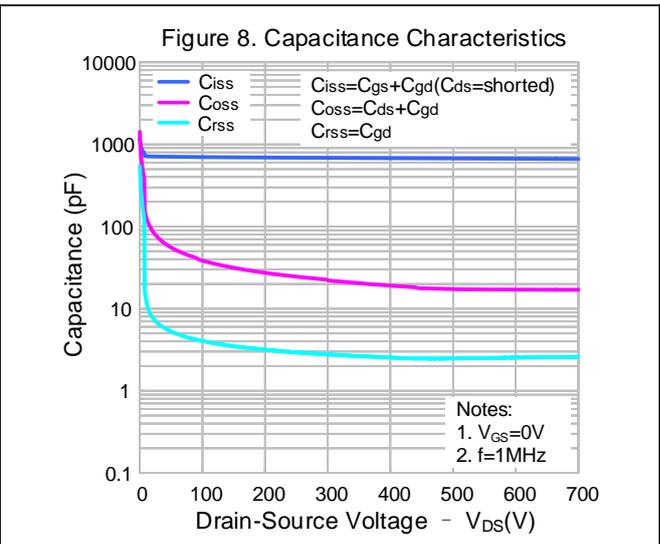
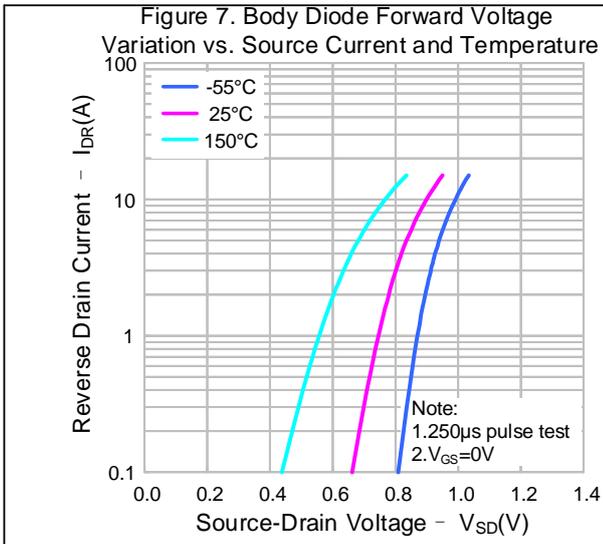
**Notes:**

- Pulse time 5μs;
- The dissipation power will change with temperature, derating above 25°C:  
0.95W/°C(TO-252-2L)(TO-251J-3L)/0.34W/°C(TO-220F-3L)
- Pulse Test: Pulse width ≤300μs, Duty cycle≤2%;
- Essentially independent of operating temperature.

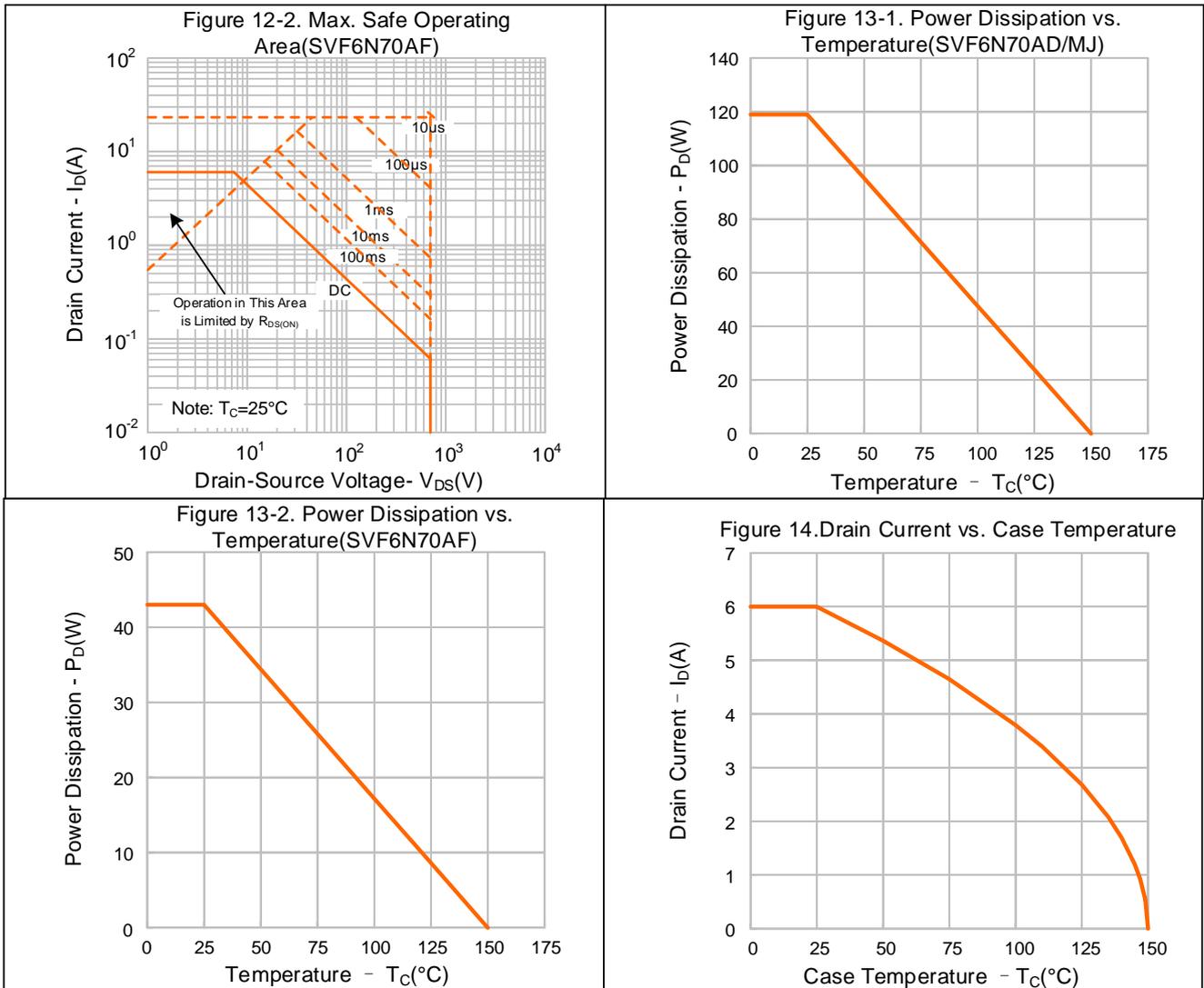
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (CONTINUED)

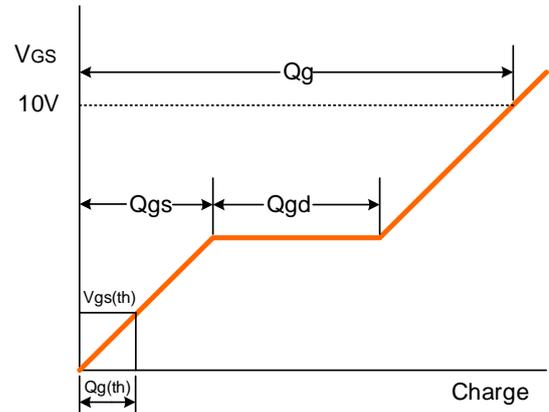
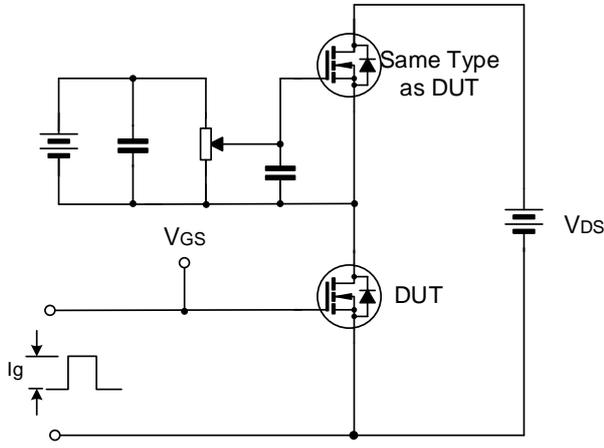


**TYPICAL CHARACTERISTICS (CONTINUED)**

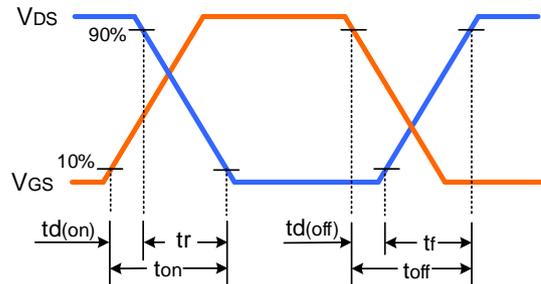
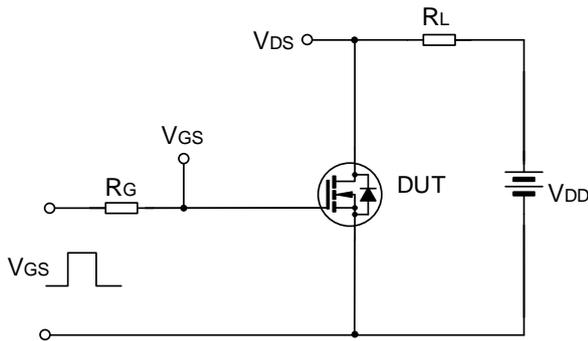


**TYPICAL TEST CIRCUIT**

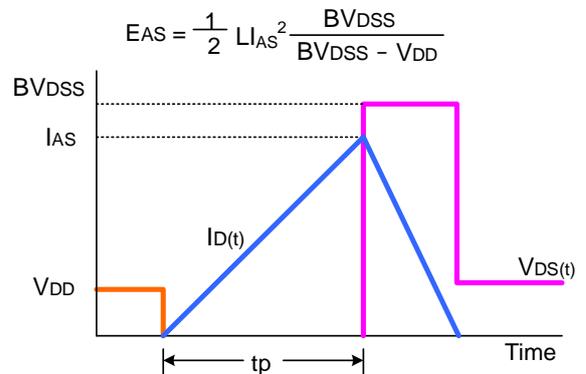
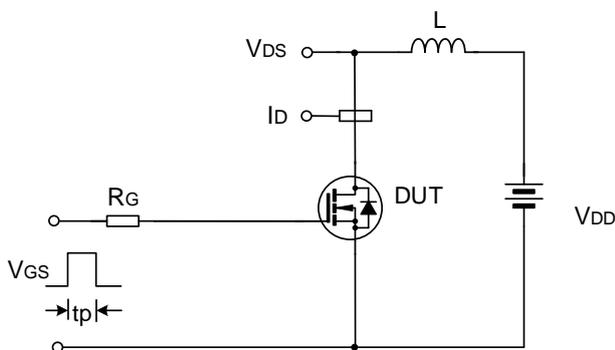
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

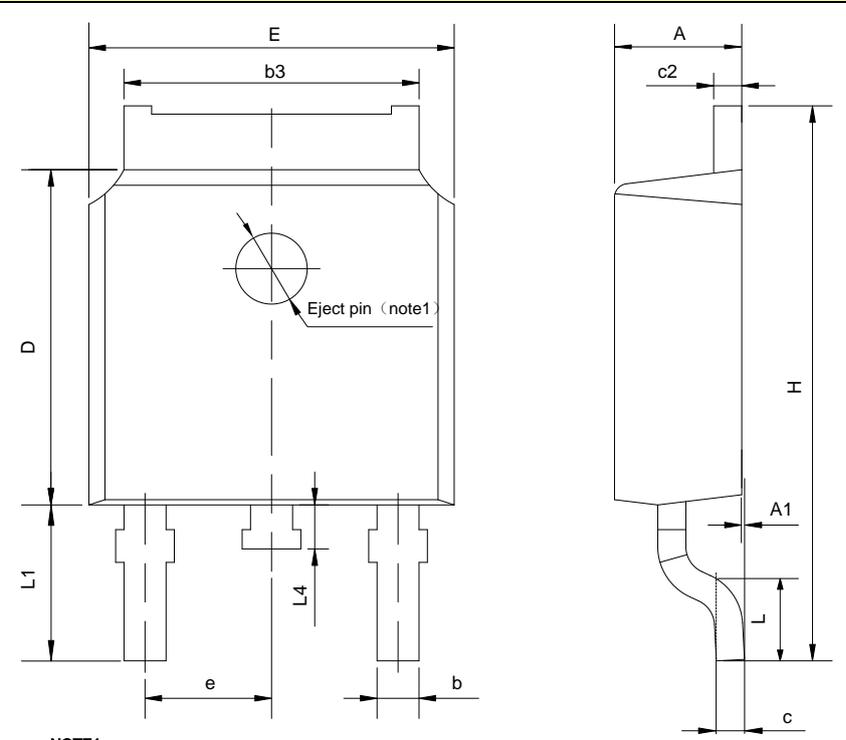


Unclamped Inductive Switching Test Circuit & Waveform



**PACKAGE OUTLINE**

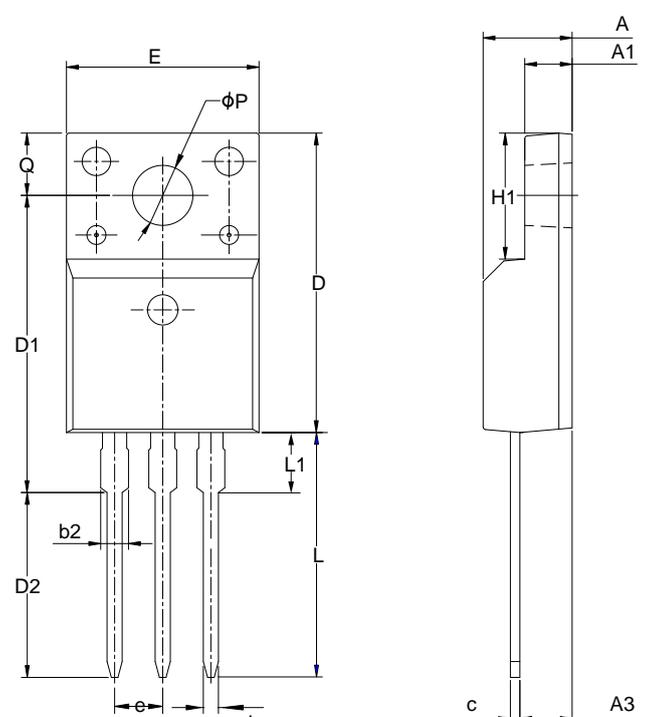
**TO-252-2L** **UNIT: mm**



**NOTE1** : There are two conditions for this position:has an eject pin or has no eject pin.

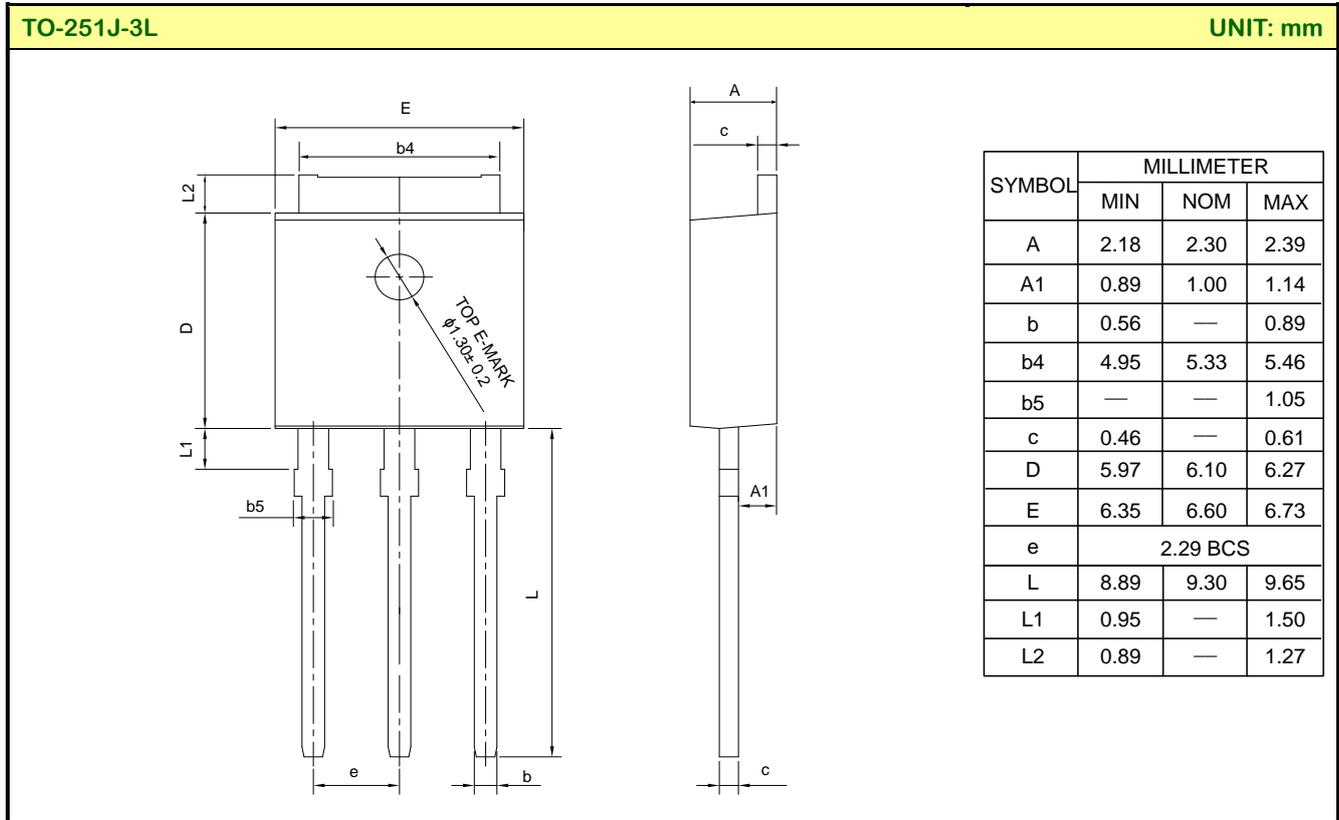
SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.10	2.30	2.50
A1	0	—	0.127
b	0.66	0.76	0.89
b3	5.10	5.33	5.46
c	0.45	—	0.65
c2	0.45	—	0.65
D	5.80	6.10	6.40
E	6.30	6.60	6.90
e	2.30TYP		
H	9.60	10.10	10.60
L	1.40	1.50	1.70
L1	2.90REF		
L4	0.60	0.80	1.00

**TO-220F-3L** **UNIT: mm**



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BSC		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	—	—	3.50
φP	3.00	3.18	3.40
Q	3.05	3.30	3.55

**PACKAGE OUTLINE (CONTINUED)**



**MOS DEVICES OPERATE NOTES:**

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

**Important notice :**

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Rev.: 1.0

Revision History:

1. First release
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